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U.S. Pat.  
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TESSERA 3.0-078

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re Patent of :  
Joseph Fjelstad :  
: Group Art Unit:  
Divisional of Application No. 08/739,303 :  
filed October 29, 1996 :  
: Examiner:  
Filed: Herewith :  
: Date February 9, 1998  
For: Semiconductor Chip Package :  
With Fan-In Leads :  
X

Assistant Commissioner for Patents  
Washington, D.C. 20231

**INFORMATION DISCLOSURE STATEMENT**

Sir:

It is respectfully requested that the references listed on the enclosed Form PTO-1449 be made of record and considered with respect to the above-referenced U.S. Patent application. Only the reference denoted by an asterisk on the Form PTO-1449 is submitted herewith. The other references listed on the Form PTO-1449 were previously submitted in Parent Application No. 08/739,303 and therefore are not being submitted herewith. Submission of the present Information Disclosure Statement should not be taken as an admission that the cited reference is legally available prior art or that the same is pertinent or material.

Respectfully submitted,

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